This is the CALICE Data Acquisition Page.

The CALICE DAQ is supposed to be generic and quite independent of the underlying detector technology. Therefore it will be easy to test several detector technologies used in the CALICE Technology Prototype which is due in 2009. The DAQ system is explained in more detail in the attachment.

Links to the subcomponents webpages can be found here:

- **Hardware**
  - List of all hardware with status and location(s)
  - lastest version of schematics
  - lastest version of FW
  - Description of calice DAQ2 system as used for the Si-W ECAL

- **Elements**
  - DAQ PC
  - ODR Off Detector Receiver
  - Clock and Control Card
  - LDA Link Data Aggregator
  - GDCC DataConcentrator Card with Giga-ethernet
  - DCC Data Concentrator Card
  - DIF Detector InterFace
  - ASICS, detector specific

- **Software**
  - Old DAQ software (DOOCS) (Deprecated)
  - C/C++ API to LDA (Deprecated)
  - new DAQ software (xDAQ)
  - Repositories
  - Calicoes

- **Overall documents**
  - Specifics for integration
  - Performances pages

- **DaqInternals (restricted to CaliceDaqGroup)**

Links to recent meetings:

- CALICE and EUDET meetings at DESY
- CALICE collaboration meeting in Manchester
- CALICE collaboration meeting in Argonne
- ECFA 2008, Warsaw
- further meetings to be added

Links to regular meetings:

- links, e.g. to Remi's meetings

-- RemiCornat - 19 Nov 2013