

Put here a description and a version of your latest FW/SW

- CCC FW [https://svn.in2p3.fr/calice/vhdl/trunk/CCC\\_VHDL/](https://svn.in2p3.fr/calice/vhdl/trunk/CCC_VHDL/)
  - ◆ `ccc_top_ramfull_veto_and_busy.jed`: CCC FW from 04/10/2011; Idem FW<--> DIF v40 + no BUSY in stop + BUSY output
- DIF FW <https://svn.in2p3.fr/calice/vhdl/trunk/>
  - ◆ for HR2:
  - ◆ for HR2b
- DCC Firmware
- LDA Firmware
- Some help
  - ◆ *Programming XILINX FGPA anywhere*

-- VincentBoudry - 11-May-2011

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This topic: CALICE > DaqFW

Topic revision: r7 - 2011-10-04 - VincentBoudry



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