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Trigger Box Mapping

The authoritative version of this document is in the **DreamTrigger.ucf** file in the <https://twiki.cern.ch/twiki/bin/view/DREAM/TriggerBox> page

Channel 13 discriminator has problems

NIM in/out ch	other signal	type	name	comment	V262 (IO) ch	V513 (IO) ch	V814 (Discr.) ch
20		NIM output	nimFastGate 3	to DRS Trig input TR1			
19		NIM output	nimFastGate 2	to DRS Trig input TR0			
18		NIM output	daqTr2	to V976 fan-out			
17		NIM output	daqTr1	to busy gate gen.			
16		NIM output	isSpill			IN 2	
15		NIM output	isPedTrig			IN 3	
14		NIM output	nimFastGate 1	to Calorimeter ADC gate (0&1)			
13		NIM output	isPhysTrig			IN 0	
12		NIM output	T1T2	T1 & T2			
11		NIM output	T1T2_Vbar	T1 & T2 & not(Veto)			
10		ECL input					
9		ECL input	T2				12
8		ECL input	T1				11
7		ECL input	veto				10
6		ECL input	ped	pedestal trigger			9
5		NIM input	nim_daqRes	DAQ reset	SHP OUT 0		
4		NIM input	nim fast gate	fast gate			
3		NIM input	nim_spill	SPS spill signal			
2		NIM input	nim_enPed	pedestal enable	OUT 3		
1		NIM input	nim_enable	trigger enable	OUT 0		
-	OUT of BUSY timing unit	-	busy	shaped daqTr1 (25 usec)		IN 1	
-	NIM scaler clear	-	DAQ clear				

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				clears scaler (at run start)	SHP OUT 1		
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-- MicheleCasella - 2014-11-30

This topic: DREAM > TriggerBoxMapping

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