

SRMv2ComparativeTests < LCG < TWiki

This page is devoted to keep track of the activity about comparative tests over all SRMv2 instances. The aim of this activity is to have an overview of the behavior of all main Storage technologies available on WLCG and equipped with SRM v2 (CASTOR, d-Cache, StoRM and DPM). The persons mainly involved on this activity are:

- *Luca Dell'Agnello (CNAF storage system manager)*
- *Vincenzo Vagnoni (CNAF/LHCb support)*
- *Angelo Carbone (CNAF/LHCb support)*
- *Luca Magnoni and Riccardo Zappi (StoRM developers)*
- *Giacinto Donvito and Vincenzo Spinoso (CMS/Bari dCache and site managers)*
- *Elisa Lanciotti and Roberto Santinelli (CERN/LHCb)*

Participant storage instances are summarized on this table (that all responsables have to keep-up-to-dated):

SITE	Flavour	Hardware and Software description	Contact	Status
CNAF	Castor	<missing description for CNAF CASTOR>	Luca.dellAgnello@cnafnOSPAMPLEASE.infn.it, giuselle.lore@cnafnOSPAMPLEASE.infn.it	under first round of test
CNAF	StoRM	2 Frontend (Opteron 2GHz - 4GB) - 1 Backend (Dual Xeon 2.4 GHz - 2 GB)	Riccardo.Zappi@cnafnOSPAMPLEASE.infn.it, Luca.Magnoni@cnafnOSPAMPLEASE.infn.it	under first round of tests
Bari	dCache	Admin Node: PIII con 1GB di RAM; DB server: Dual Xeon PIV 3.0GHz con 2 GB di RAM disco EIDE 100GB; SRM servers: (da 1 a 2/3 macchine) Dual Opteron Dual Core con 4 GB di RAM e disco SATA da 80GB; dCache 1.8.0-x with Chimera NameServer.	Giacinto.Donvito@baNOSPAMPLEASE.infn.it	under first round of test
CERN(?)	DPM		jean-philippe.baud@cernNOSPAMPLEASE.ch	requested to the responsible awaiting for.

As a first step the tests going to be performed are based on the Elisa's TestSuite used previously to study the StoRM storage. The plans for this activity consist in extending the scope of the stress tests with deeper studies on the performances of various storage solutions, their optimizations and their readiness to sustain a HEP production like use case. The tests planned differ from other scheduled SRM2 testing activities, e.g. from the GSSD working group one, since we are more oriented in stress-testing the performance of the systems up to extreme conditions, rather than their functionalities and daily availability, and to study the feasibility of the LHC analysis and reprocessing activities (mainly LHCb and CMS ones since the experiment members of this group are from these two VOs) on T0D1 storage systems by means of custom experiment applications. These tests also differ by the internal LHC VO SRM2 testing activities because the latter are mainly oriented to verifying the integration of the VO production system with the underlying production infrastructure and to test (with official production tools) the readiness of SRM2 endpoints.

Minutes available of previous meetings are available at: [Minutes](#)

Logbook of the tests are available at: [Logbook](#)

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