**Idea**

To be able to process data at 30 MHz, the possibility of running the full HLT1 on GPUs is currently being studied.

The repository of the project is [Allen](https://github.com/lhcb/Allen).

A full HLT1 implementation on GPUs is available and was documented in detail for the accelerator's review in June 2019:

- [review document](https://github.com/lhcb/Allen/blob/master/Design/FullHLT1/readme.md)
- [Link to review Indico page](https://indico.cern.ch/conferenceDisplay.py?confId=10878)

If you want to join the effort, you are more than welcome. Please contact Daniel (dcampora@cern.ch) and/or Dorothea (dorothea.vom.bruch@cern.ch).

The mailing list is [lhcb-rta-accelerators](mailto:lhcb-rta-accelerators@cern.ch).

**To do list**

The list of items to be worked on, currently being worked on, and finished can be found here together with the people to whom they are assigned: [https://gitlab.cern.ch/lhcb-parallelization/Allen/boards?](https://gitlab.cern.ch/lhcb-parallelization/Allen/boards?)

**Past meetings**

Past meetings since Allen is part of WP6 of RTA:[https://indico.cern.ch/category/10878/](https://indico.cern.ch/category/10878/)

Past meetings before Allen became part of WP6 of RTA: [https://indico.cern.ch/category/7616/](https://indico.cern.ch/category/7616/)

Minutes from the meetings:

- 26.4.2018
- 12.7.2018
- 23.10.2018
- 27.11.2018

-- DorotheaVomBruch - 2018-03-13