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Calorimeter Electronics Status

Below are compiled useful information addressing the current status of the calorimeter Electronics during the installation and the commissioning phase toward the final setup. The expected final readout setup is described [here](#) and is summarized in the dedicated LHCbCaloReadout wiki page.

ECAL/HCAL

FEB

- *Side C* Information on FEB, as of October 26th, is found in CROC test below
- *Side A*

CROC

- *Side C*

		Date	Resolved on
ECAL_8	impossible to have any ECS communication with the FEBs. No test performed with this CROC. Problem had been already seen with 2 different CROC v2 (backplane, SPECS bus problem, A board affecting the SPECS bus, reset bus ???) In Orsay we found that the glue was not programmed correctly (checksum FFFF) the glue output was causing the Specs bus slave to master to be reserved by this board in slot 7 at all time preventing other boards to talk. Board now OK ready to be sent back to CERN	22 Oct 2007	26 Oct 2007
ECAL_10	Data are dubious	22	26 Oct

	<p>for several FEBs in synchronisation test. Noise tests are fine with usual time settings. CROC_28 has been replaced by CROC_24 in this crate. Still the synchronisation is dubious although the Noise test is fine. We observe that the board in slot 18 has a different Synchronisation plot from the others : the plot is shifted downward of at least 4ns. We never observed that at LAL or on the other crates. CROC_24 left in this crate ECAL 10 slot 18 board 050 showed this 4ns shift it was taken to Orsay and tested => no problem seen. T (JL) suspect a bus problem to be checked at CERN</p>	Oct 2007	2007
ECAL_11	<p>board in slot 2 does not work properly. Board/Slot problem ??? This had been seen with CROC v2. Test of the CROC v4 was done by masking this board -> OK</p>	22 Oct 2007	26 Oct 2007

	The +5V fuse is burnt, the board 096 is in office 002 building 1 waiting for the fuse to be replaced		
ECAL_14	board in slot 18 (FEB number 217) provides wrong data in pattern mode (e.g. synchronisation test). Pedestal is fine and acquisition is correct in this mode. Synchronisation requires ram pattern. Even configuring the board 'by hand', I could not have correct ram patterns at the level of the CROC or looking at the spy ram of the sequencer. Notice that event headers are ok but data look corrupted. Same test done with FEB in slot 17 is ok The CROC and board were retested on Oct 17th no problem were found??? the FEboard was left in slot 18	22 Oct 2007	26 Oct 2007

• *Side A*

		Date	Resolved on
ECAL_15 ..._25	To be installed	22 Oct	

	2007	
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Backplanes

- *Side C*
- *Side A*

Power Supply

- *Side C*
- *Side A*

Spares

FEB

CROC

Backplanes

Power Supply

Cabling

Due to backplane problem the following FEB are currently moved with respect to their expected location.

Ecal crate #16 : FEB#12 and FEB#13 moved FROM slot 17 and 18 TO slot 18 and 19

Ecal crate #15 : FEB#13 moved FROM slot 18 TO slot 19 **WARNING this slot is to be used by one LEDTSB**

Hcal crate #22 : FEB#14 moved FROM slot 19 TO slot 20 **NO problem as it's a PIN readout FEB**

Comment : backplane from Ecal crate#15 and #16 need to be repaired. Moving FEB affects L0Calo.

PRS/SPD

FEB

- *Side C*
- *Side A*

CROC

- *Side C*

		Date	Resolved on
PRS_0 ... 3	To be installed	22 Oct 2007	

- *Side A*

		Date	Resolved on
PRS_4 ... 7	To be installed	22 Oct 2007	

Backplanes

- *Side C*
- *Side A*

Power Supply

- *Side C*
- *Side A*

Spares

FEB

CROC

Backplanes

Power Supply

Cabling

TELL1

Tell1 boards and crates

Trigger

Trigger Validation Boards

* Scan of TVB inputs:

Board	Scan of HCAL Inputs	Scan of ECAL Inputs
0	HCAL0 PatternA, HCAL0 PatternB	Crate 8 slot 0-7
	HCAL1 PatternA, HCAL1 PatternB	
	HCAL2 PatternA, HCAL2 PatternB	
	HCAL3 PatternA, HCAL3 PatternB	
1	HCAL0 PatternA, HCAL0 PatternB	Crate 8 slot 11-18
	HCAL1 PatternA, HCAL1 PatternB	
	HCAL2 PatternA, HCAL2 PatternB	
2	HCAL0 PatternA, HCAL0 PatternB	Crate 9 slot 0-7
	HCAL1 PatternA, HCAL1 PatternB	
	HCAL2 PatternA, HCAL2 PatternB	
	HCAL3 PatternA, HCAL3 PatternB	
3	HCAL0 PatternA, HCAL0 PatternB	Crate 9 slot 11-18
	HCAL1 PatternA, HCAL1 PatternB	
	HCAL2 PatternA, HCAL2 PatternB	
4	HCAL0 PatternA, HCAL0 PatternB	Crate 10 slot 0-7
	HCAL1 PatternA, HCAL1 PatternB	
	HCAL2 PatternA, HCAL2 PatternB	
	HCAL3 PatternA, HCAL3 PatternB	
5	HCAL0 PatternA, HCAL0 PatternB	Crate 10 slot 11-18
	HCAL1 PatternA, HCAL1 PatternB	
	HCAL2 PatternA, HCAL2 PatternB	
	HCAL3 PatternA, HCAL3 PatternB	
6	HCAL0 PatternA, HCAL0 PatternB	Crate 11 slot 0-7
	HCAL1 PatternA, HCAL1 PatternB	
	HCAL2 PatternA, HCAL2 PatternB	
7	HCAL0 PatternA, HCAL0 PatternB	Crate 11 slot 11-18
	HCAL1 PatternA, HCAL1 PatternB	
	HCAL2 PatternA, HCAL2 PatternB	
8	HCAL0 PatternA, HCAL0 PatternB	Crate 12 slot 0-7
	HCAL1 PatternA, HCAL1 PatternB	
	HCAL2 PatternA, HCAL2 PatternB	
9	HCAL0 PatternA, HCAL0 PatternB	Crate 12 slot 11-18
	HCAL1 PatternA, HCAL1 PatternB	
	HCAL3 PatternA, HCAL3 PatternB	
10	HCAL0 PatternA, HCAL0 PatternB	Crate 13 slot 0-7
11	HCAL0 PatternA, HCAL0 PatternB	Crate 13 slot 11-18
	HCAL1 PatternA, HCAL1 PatternB	
12	HCAL0 PatternA, HCAL0 PatternB	Crate 14 slot 0-7
	HCAL1 PatternA, HCAL1 PatternB	
13	HCAL0 PatternA, HCAL0 PatternB	Crate 14 slot 11-18

-- FredericMachefert - 22 Oct 2007

This topic: LHCb > LHCbElectronicsStatus

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