

OnlineActivities < LHCb < TWiki

<b>Lead group</b>	Computing
<b>Participating groups</b>	Computing, Online
<b>Description</b>	Port of Software stack to ARM64 and Openpower
<b>Required FTE</b>	0.5
<b>Available FTE</b>	0.5
<b>Deadline</b>	Q3/2018
<b>Dependencies</b>	MiniBrunel/MiniMoore
<b>People currently involved</b>	Laura
<b>New effort required?</b>	No
<b>Link to documentation</b>	

<b>Lead group</b>	Computing
<b>Participating groups</b>	Computing, Online
<b>Description</b>	Profile ARM64 & Openpower
<b>Required FTE</b>	0.5
<b>Available FTE</b>	0.5
<b>Deadline</b>	Q4/2018
<b>Dependencies</b>	Port of platforms
<b>People currently involved</b>	Laura
<b>New effort required?</b>	No
<b>Link to documentation</b>	

<b>Lead group</b>	Online
<b>Participating groups</b>	Computing, HLT, Online, T&A
<b>Description</b>	Evaluate desirability of proposed accelerator solutions
<b>Required FTE</b>	N/A
<b>Available FTE</b>	N/A
<b>Deadline</b>	Q4/2018
<b>Dependencies</b>	HLT, T&A, Computing
<b>People currently involved</b>	Several teams are developing FPGA and GPU based accelerators, the online, HLT, and T&A teams are developing benchmarks and boundary conditions within which to evaluate them
<b>New effort required?</b>	
<b>Link to documentation</b>	<a href="https://indico.cern.ch/event/732751/contributions/3021754/attachments/1663909/2666726/note.pdf">https://indico.cern.ch/event/732751/contributions/3021754/attachments/1663909/2666726/note.pdf</a>

<b>Lead group</b>	Online
<b>Participating groups</b>	Computing, T&A, PID, HLT
<b>Description</b>	Develop baseline hardware archs
<b>Required FTE</b>	we will know after alternative archs / accelerators
<b>Available FTE</b>	0
<b>Deadline</b>	Q3/2020
<b>Dependencies</b>	Alternative architecture port, accelerator devel
<b>People currently involved</b>	0
<b>New effort required?</b>	Yes
<b>Link to documentation</b>	

-- VladimirGligorov - 2018-06-09

This topic: LHCb > OnlineActivities

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