

## Slice Test - PVSS operation of the system

### General start (only at the beginning of the day)

1. Log on to a Windows console in the online network and start Exceed.
2. Open the following directory: G:\online\vecs\Shortcuts\VELO
3. Double click on VEECS1\_UI\_FSM.
4. Expand the item VEECS1 clicking on "+" in order to get the item VELO.
5. Select the VELO item and right click: select View.
6. Double-click on the VELOA(C) button (bottom left).
7. If the top padlock (VELOA(C)) is open, click on it and Take.
8. The system should be in the state "NOT\_ALLOCATED" (if not select "DEALLOCATE").
9. Select "Allocate" by pressing on "NOT\_ALLOCATED". This assigns a readout supervisor (ODIN) to the VELO half providing clock and fast signals. The system should be afterwards in the state "NOT\_READY".

### Configure Control board and hybrid

1. Double-click on VELOA(C)\_DAQ. You will get a big panel with Control Boards, VeloModules and Tell1s.
2. Click on the Control Board you want to use (See e.g. VELOC\_DAQ.JPG).
3. You will get the Control Board panel (See e.g. VELOC\_DAQ\_CB5.JPG).
4. Check that the specs node (vedaqa(c)01) is green (running); if not, call the expert.
5. Click on the top button inside the Control Board frame and choose GetReady. Wait until the text on the button becomes . Click again the same button and choose LV ON: you will power the low voltage. The software automatically finds which port of the Control Board is connected to the Repeater Board, and colors the corresponding numbered label blue. Click on that.
6. You will get the Hybrid control panel (See e.g. CB5Hybrid1.JPG)
7. Check that the specs node (vedaqa(c)01) and the DNS node (ecs01) are both green; if not, call the expert.
8. In the bottom left white field there is a list of recipes: select the one you want to use (see below).
9. Click the top button inside the Beetle Selection frame and choose ON. Wait for the configuration and powering of all 16 beetles. If the button becomes blue, you're done!

### Configuration of TFC and TELL1s and data taking (delay scan: see separate instructions)

1. Go back to the main menu.
2. Select the recipe you want to use in the pull down menu below "Activity:" (see below).
3. Configure the system by selecting "Configure" after clicking on "NOT\_READY". This should bring the subsystems VELOA(C)\_DAQ, VELOA(C)\_Runinfo and VELOA(C)\_TFC into the state "READY".
4. Select "Limited to" below "Max Nr. Triggers:" and enter 10000.
5. Select "No Steps" below "Automated Run with Steps:".
6. Start the system by selecting "START\_RUN" after clicking on "READY". This should bring the subsystems VELOA(C)\_DAQ and VELOA(C)\_Runinfo into the state "RUNNING" and VELOA(C)\_TFC into the state "ACTIVE".
7. **This is the latest moment to start the eventbuilder!**
8. Start the run by selecting "GO".
9. Wait until the run is automatically stopped. The subsystems VELOA(C)\_DAQ, VELOA(C)\_Runinfo and VELOA(C)\_TFC should be in the state "READY".
10. **Stop the eventbuilder!**

11. Reset the system by selecting "RESET" after clicking on "READY". This should bring the subsystems VELOA(C)\_DAQ, VELOA(C)\_Runinfo and VELOA(C)\_TFC into the state "NOT\_READY".

## Available Recipes

- DELAYSCAN: digitisation delay scan
- CABLETEST: test of the cabling by injection a bit pattern of test pulses
- PHYSICSTP: standard data taking with test pulses in channels 4 and 23 of each link
- PHYISCSNTP: standard data taking without test pulses

-- StefanoDeCapua - 08 Feb 2008

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This topic: LHCb > Pvss

Topic revision: r5 - 2008-04-23 - OlafBehrendt



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