

Power Off procedure for shifters

If the HV panels are not on, please read VELOPowerOnShift to see how to navigate to them. The same applies for LV.

Step 1: Turn of the High Voltage (HV)

1. Navigate back to the VELOA_HV panel and click Go_OFF:


The screenshot shows the 'VELOA_HV: TOP' control interface. At the top, the 'System' is 'VELOA_HV' and its 'State' is 'READY'. A dropdown menu is open, showing options: 'Go_STANDBY2', 'Go_STANDBY1', 'Go_OFF' (highlighted in blue), 'Do_Emergency_OFF', and 'Reset'. Below this, a table lists five sub-systems, all with a 'READY' state:

Sub-System	State
VELOA_HV_BOARD00	READY
VELOA_HV_BOARD01	READY
VELOA_HV_BOARD02	READY
VELOA_HV_BOARD03	READY
VELOA_HV_BOARD04	READY
VELOA_HV_BOARD05	READY

On the right, there are detailed views for each board (BOARD00 to BOARD05), each showing a grid of channels with their respective 'AT' and 'AB' values. For example, VELOA_HV_BOARD00 shows channels like PU01_AB (0.0/0.0), PU02_AT (0.0/0.0), VL01_AT (100.0/40.2), etc. A 'CAN HeartBeat' indicator shows a value of 39. At the bottom left, there is a 'Messages' section which is currently empty.

2. The status will change to RAMPING_OFF

VELOA_HV: TOP



System

VELOA_HV

State

HOT_READY

Sub-System	State	
VELOA_HV_BOARD00	RAMPING_OFF	
VELOA_HV_BOARD01	RAMPING_OFF	
VELOA_HV_BOARD02	RAMPING_OFF	
VELOA_HV_BOARD03	RAMPING_OFF	
VELOA_HV_BOARD04	RAMPING_OFF	
VELOA_HV_BOARD05	RAMPING_OFF	

Iseg Crate

CAN HeartBeat

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Archived Trends

- VELOA_HV_BOARD00
- VELOA_HV_BOARD00_CH0

VELOA_HV_BOARD00					
PU01_AB	PU02_AT	VL01_AT	VL01_AB	VL02_AT	VL02_AB
0.0/ 0.0	0.0/ 0.0	89.2/38.1	89.3/ 0.0	89.3/ 1.3	89.3/ 1.3
ch00	ch01	ch02	ch03	ch04	ch05

VELOA_HV_BOARD01					
VL04_AT	VL04_AB	VL05_AT	VL05_AB	VL06_AT	VL06_AB
89.2/ 1.8	89.2/ 1.3	89.2/ 0.7	89.1/ 3.0	89.1/ 1.7	89.1/ 1.7
ch00	ch01	ch02	ch03	ch04	ch05

VELOA_HV_BOARD02					
VL08_AT	VL08_AB	VL09_AT	VL09_AB	VL10_AT	VL10_AB
89.1/ 4.8	89.1/ 1.4	89.1/ 1.1	89.1/ 1.9	89.1/ 2.0	89.0/ 1.4
ch00	ch01	ch02	ch03	ch04	ch05

VELOA_HV_BOARD03					
VL12_AT	VL12_AB	VL13_AT	VL13_AB	VL14_AT	VL14_AB
89.0/ 0.9	89.0/ 1.4	88.9/ 1.2	88.8/ 1.5	88.8/ 1.0	88.9/ 1.4
ch00	ch01	ch02	ch03	ch04	ch05

VELOA_HV_BOARD04					
VL16_AT	VL16_AB	VL19_AT	VL19_AB	VL22_AT	VL22_AB
88.9/ 0.9	88.9/ 2.8	88.8/ 1.5	88.8/ 1.7	88.8/ 0.5	88.8/ 1.5
ch00	ch01	ch02	ch03	ch04	ch05

VELOA_HV_BOARD05					
VL24_AT	VL24_AB	VL25_AT	VL25_AB	undefined	undefined
88.7/ 1.2	88.8/ 2.0	88.9/ 2.1	89.0/ 1.5	0.0/ 0.0	0.0/ 0.0
ch00	ch01	ch02	ch03	ch04	ch05

Messages

3. The status will change to OFF after it finishes ramping down

The screenshot shows the VELOA_HV control interface. At the top, the system name is 'VELOA_HV' and its state is 'OFF'. Below this, there is a table of sub-systems, all of which are also 'OFF'. To the right, there are indicators for 'Iseg Crate' and 'CAN HeartBeat' (102). The main area displays detailed status for five boards (BOARD00 to BOARD05), each with a grid of channel readings (e.g., PU01_AB, PU02_AT, VL01_AT, etc.) showing '0.0/0.0'. A 'Messages' section is visible at the bottom.

4. Click the lock at the top and click ReleaseAll to release the control of the HV

The 'Modes' dialog box for VELOC_HV is shown. It indicates 'Is InLocal' and contains two buttons: 'Release' and 'ReleaseAll', each with a lock icon to its left. A dropdown arrow is visible at the bottom of the dialog.

5. Repeat HV voltage off for the C side

Step 2: Turn of the Low Voltage (LV)

⚠ POWER OFF THE C SIDE FIRST ⚠


1. Navigate to the VELOC_DCS_LV panel and click Switch_OFF.



The screenshot shows the VELOC_DCS_LV control panel. At the top, the system name 'VELOC_DCS_LV' is displayed with a 'READY' status. A dropdown menu is open, showing 'Switch_OFF' and 'Reset' options. Below this is a table of sub-systems, all of which are in a 'READY' state. To the right, there are several panels for individual EBs (VELOC_DCS_LV_EB00 through VELOC_DCS_LV_EB09), each containing four status indicators (CT and CB pairs). The bottom of the panel features a 'Messages' section.

Sub-System	State
VELOC_DCS_LV_PS	READY
VELOC_DCS_LV_EB00	READY
VELOC_DCS_LV_EB01	READY
VELOC_DCS_LV_EB02	READY
VELOC_DCS_LV_EB03	READY
VELOC_DCS_LV_EB04	READY
VELOC_DCS_LV_EB05	READY
VELOC_DCS_LV_EB06	READY
VELOC_DCS_LV_EB07	READY
VELOC_DCS_LV_EB08	READY
VELOC_DCS_LV_EB09	READY
VELOC_DCS_LV_ConfDB	READY



2. The status will change to NOT_READY:


VELOCDCS_LV: TOP











System: VELOC_DCS_LV State: **NOT_READY**  





Sub-System	State	
VELOCDCS_LV_PS	OFF	✓
VELOCDCS_LV_EB00	OFF	✓
VELOCDCS_LV_EB01	OFF	✓
VELOCDCS_LV_EB02	OFF	✓
VELOCDCS_LV_EB03	OFF	✓
VELOCDCS_LV_EB04	OFF	✓
VELOCDCS_LV_EB05	OFF	✓
VELOCDCS_LV_EB06	OFF	✓
VELOCDCS_LV_EB07	OFF	✓
VELOCDCS_LV_EB08	OFF	✓
VELOCDCS_LV_EB09	OFF	✓
VELOCDCS_LV_ConfDB	READY	✓





Recipe and Scan Managers  





Archived Trends 






VELOCDCS_LV_EB00    

VELOCDCS_LV_EB02    

VELOCDCS_LV_EB04    

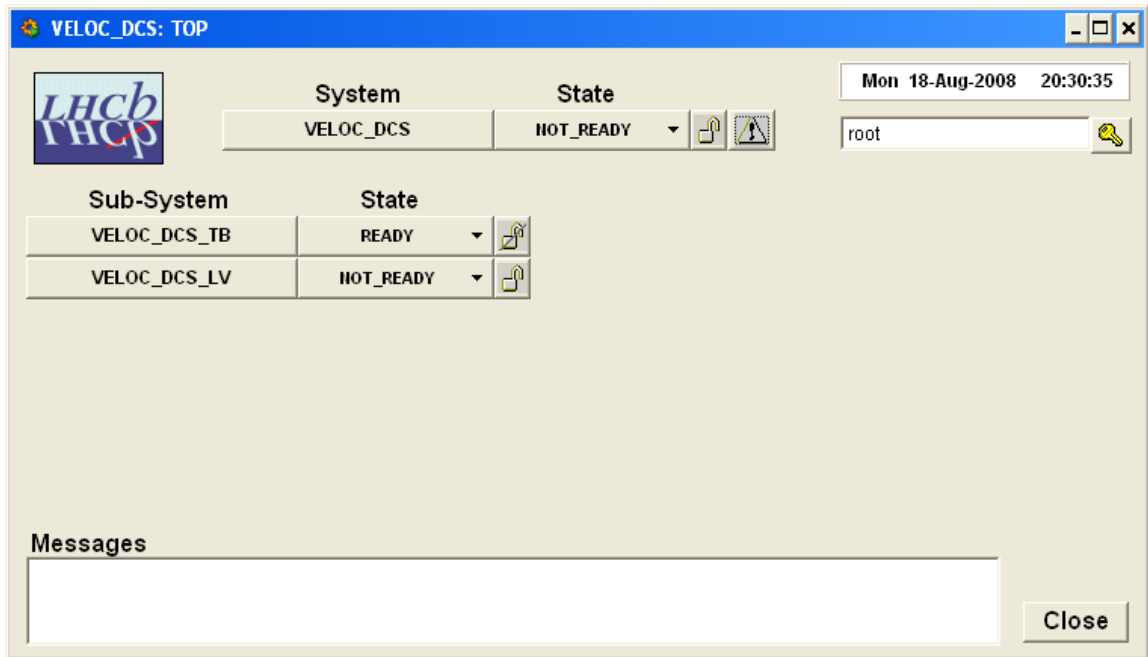
VELOCDCS_LV_EB06    

VELOCDCS_LV_EB08    

VELOCDCS_LV_EB09     

Messages

1 Navigate to VELOC_DCS top panel and Click the lock and ReleaseAll



1 Repeat for the A side from the VELOA_DCS_LV panel.

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