

- arrived at CERN on week 33 / 2013
 - visual inspection OK
 - installation as in SR1:
 - ◆ FPGA versions currently loaded on this Rev.f ROD are...
 - ROD Controller: f38
 - ROD Formatter: f50-0
 - ROD EFB: f30
 - ROD Router: f2e
 - MDSP:
 - /work/pc-sct-mn-40/sctdaq/tdaq-04-00-01/installations/bruce/SctRodD
 - tested ok
 - Installed in USA15 on 04 September 2013
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This topic: Main > AtlasSiliconROD_310

Topic revision: r2 - 2013-09-04 - KristinLohwasser



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