

- arrived at CERN on week 33 / 2013
 - visual inspection OK
 - installation as in SR1:
 - ◆ FPGA versions currently loaded on this Rev.f ROD are...
 - ROD Controller: f38
 - ROD Formatter: f50-0
 - ROD EFB: f30
 - ROD Router: f2e
 - MDSP:
/work/pc-sct-mon-40/sctdaq/tdaq-04-00-01/installations/bruce/SctRodDaq_32/RodDaq/Dsp/build/bin/
 - tested ok
 - Installed in USA15 on 04 September 2013
 - Fault 21/11/2014 cannot write firmware. Extracted from USA15 for debugging in bat104
-

This topic: [Main > AtlasSiliconROD_337](#)

Topic revision: r3 - 2014-11-21 - DaveRobinson



Copyright &© 2008-2019 by the contributing authors. All material on this collaboration platform is the property of the contributing authors.

Ideas, requests, problems regarding TWiki? Send feedback