

To Do List for 2009 Global Run analysis

- Look for the change in delays when we moved latency from 142 to 143 and then when we moved to DECO
- History of missing components
- Check if single strips are masked from DAQ
- History of the FED-detected errors:
 - ◆ Recovery actions: voltage settings, masked channels
 - ◆ Out Of Sync TEC modules
- History of pedestal and noise
- Common Mode monitoring
- History of bad components
 - ◆ Hot Components
 - ◆ DQM bad modules
- Signal normalization results
 - ◆ low gain TEC- modules
- TEC- noise: when was wrong and when was fixed and why it was wrong

-- AndreaVenturi - 2009-09-17

This topic: Main > ToDoGR2009

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