

S. Ramberger, M. Schuh, G. DeMichele and P. Tirado

CCDTL

- Circuit model almost complete, losses still have to be included
- Detailed presentation next meeting
- Measurements on the CCDTL planned

DTL

- High Power Test ongoing
 - ◆ Operating with 50Hz and 1.5ms pulses. Breakdowns observed (max $P_{CAV}=170kW$)
 - ◆ X-ray measurements done with the detector on the side and on beam axis at different powers (waiting for data)
 - ◆ Operating with 30Hz and 1.5ms pulses. Breakdowns observed (max $P_{CAV}=180kW$)
 - ◆ Until now the designed power (220kW, taking into account the 80% of the Q) has been reached up to 4% d.c. (20Hz, 2msec)
 - ◆ CW run with 24kW over weekend
 - ◇ Max temperature: 70°C for the end-cones
- At the L4 BCC-8 meeting, beam dynamics people presented their commissioning strategy which foresees for commissioning that the DTL tank1 is driven at up to 110% the nominal voltage however at a beam current close to 0. The feasibility needs to be checked.

HOM

- High frequency chopping causes a significant emittance growth, if Q_{ex} exceeds 10^5 . Only certain high frequency chopping patterns are foreseen in SPL operation and can be fixed. This would allow tuning the HOMs away from resonance lines with slight cavity design changes.

-- MarcelSchuh - 20-Nov-2009

This topic: SPL > Minutes20November2009

Topic revision: r4 - 2009-11-23 - SuitbertRamberger



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