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This is the TWIKI page of ISOTDAQ2018 in Vienna

Wed 14 February Wed 22 February 2018

The main purpose of this Twiki is to share information relevant for the organization of the school

Related Web sites

School home page: <https://indico.cern.ch/event/643308/>

SharePoint site: <https://espace2013.cern.ch/isotdaq-sharepoint>

Kick off meeting with Vienna and the ISOTDAQ staff

- Industrial participation (sponsors)
- Candidate tutors
- Candidate lecturers
- Changes to the program (taking into account the feedback from previous schools)
- Advertising channels

--> Material and minutes : <https://indico.cern.ch/event/648536/>

Action items

Item	target date	to be done by	Done	Remarks
Establish a financial plan	1 July	Local organizers	X	Done but to be refined during the year
Fix the final date and duration of the school	1 August	Local organizers	X	Done
Update the new e-group "isotdaq-staff@cern.ch"	1 August	Hannes	X	Vienna colleagues added
Create a Web site	1 August	Local organizers	X	Includes a tentative schedule (indico)
Create a poster	1 August	Local organizers	X	-
Look for sponsors	-	COC, LOC, volunteers	X	EP, LHC experiments, NI???
Open CERN conference account	1 August	BvH	X	-
Organize Kick-Off meeting	1 July	BvH	X	-
Collect channels for making publicity	1 August	All	X	-
Start publicity campaign	1 August	All	X	-
Confirm the availability of the lecturers and tutors	1 October	BvH	X	-
Look for new tutors to fill the holes	1 October	COC	X	-
Look for new lecturers to fill the holes	1 October	COC	X	-
Organize training of new tutors	1 October	COC		-

Set up the exercises by using local material	15 October	All		experienced tutors to provide help
Shipping of material to Vienna	15 November	COC	X	What will be shipped depends on the status of the locally set up exercises. According to S. Krattinger shipping (land based) takes 3 weeks

Issues for later

- Think about the home-work (C programming) and how to make sure that all students do it
- Decide on policy for funding the travel of tutors and lecturers

Teachers and tutors - Availability for ISOTDAQ2018

Name	Lecturer 2017	Exercise tutor 2017	Availability 2018	Arrival and departure dates	Funding
Ozgur Cobanoglu (CERN / PH-ESE-ME, now with iSKO Technology Development, SANKO Holding)	YES	NO	YES	TBD	ISOTDAQ (travel and lodging for 2 nights)
Markus Joos (CERN /ATLAS)	YES	YES	YES	11.2. - 23.2.	CERN
Enrico Pasqualucci (INFN/ATLAS)	YES	YES	YES	13.2 - 22.2	INFN
Francesca Pastore (RHUL/ ATLAS)	YES	YES	YES	TBD	TBD
Hannes Sakulin (CERN/CMS)	YES	NO	YES	13.2. - 20.2.	CERN
Christophe Haen (CERN)	NO	YES	TBD	TBD	TBD
Andrea Negri (INFN/ATLAS)	YES	YES	YES	13/02 - 22/02	INFN
Kostas Kordas	YES	YES	YES	TBD	TBD
Erkcan Ozcan	YES	NO	YES, if needed.	TBD	TBD
Mauricio Feo	YES	YES	YES	TBD	TBD
Roberto Ferrari	YES	YES	YES	TBD	TBD
Manoel Barros Marin	YES	YES	YES	13/02 - 22/02	CERN BI / ISOTDAQ
Gokhan Unel	YES	NO	YES	TBD	TBD
Martin Purschke	YES	NO	YES	TBD	ISOTDAQ (travel)
Serguei Kolos	YES	NO	YES	TBD	ISOTDAQ (travel)
Paolo Durante	YES	NO	YES	TBD	CERN
Fabrice Le Goff	NO	YES	YES	13/02 - 22/02	CERN
Alessandro Thea (RAL/CMS)	YES	YES	YES	13/02 - 16/02	RAL
Barthélemy von Haller	NO	NO	YES, if needed	13/02 - 17/02	CERN
Petr Zejdl	NO	YES	YES	18.2. - 22.2.	CERN
Cristovao Barreto	NO	YES	YES	TBD	CERN
Tommaso Colombo	YES	NO	YES	TBD	CERN
Roland Sipos	NO	YES	YES, if needed.	TBD	TBD
Gianluca Lamanna	YES	NO	YES	TBD	TBD
Dominique Gigi	NO	NO	YES	13.2-18.2	CERN
Niko Neufeld	NO	NO	YES	TBD	CERN

Manfred Jeitler	NO	NO	YES	TBD	HEPHY
Sophie Baron	NO	NO	YES	TBD	CERN
Branislav Ristic	NO	NO	YES	13/02 - 22/02	ISOTDAQ
Enrico Gamberini	NO	NO	YES	13/02 - 22/02	CERN
Simon Spannagel	NO	NO	YES	TBD	his institute

Overview of lectures

Number	Duration	Day	Title (Amsterdam)	confirmed or likely Lecturer (Vienna)
1	45	14.2.	Introduction to data acquisition	Andrea Negri
2	45	14.2.	Networking for data acquisition systems	Fabrice
3	60	14.2.	Introduction to VME bus	Markus Joos
4	60	14.2.	Introduction to trigger	Alessandro Thea
5	60	14.2.	Trigger hardware	Vienna (Jeitler)
6	60	14.2.	Microcontrollers	Mauricio Feo
7	60	14.2.	PCIexpress	Paolo Durante
8	60	14.2.	Introduction to FPGAs	Hannes Sakulin
9	60	15.2.	Modular electronics	Markus Joos
10	60	15.2.	DAQ hardware	Kostas Kordas
11	60	15.2.	LabVIEW	Cristovao Barreto
12	60	16.2.	Introduction to detector readout	Gokhan Unel
13	60	16.2.	Timing for DAQ	Sophie Baron
14	60	16.2.	Programming for today's physicist and engineers	Alessandro Thea
15	60	17.2.	TDAQ design: from test beam to medium size experiment	Roberto Ferrari
16	60	17.2.	DAQ software	Enrico Pasqualucci
17	60	17.2.	Optical links	Paolo Durante
18	60	19.2.	A scalable, portable DAQ system design	Martin Purschke
19	60	19.2.	An Introduction to medical imaging devices	Martin Purschke
20	90	19.2.	Introduction to the design of full-custom Front-End & data transmission ASICs	Ozgur Cobanoglu
21	60	20.2.	GPU in HEP: online high quality trigger processing	Gianluca Lamanna
22	60	20.2.	Advanced FPGA programming	Manoel Barros Marin
23	60	20.2.	Continuous DAQ systems (DUNE, protoDUNE)	Enrico Gamberini
24	60	21.2.	The Trigger and DAQ system of the EUDET-type beam telescopes	Simon Spannagel
25	60	22.2.	Intelligent triggering: pattern recognition with Associative Memories and other tools	Andrea Negri
26	60	22.2.	Design and implementation of a monitoring system	Serguei Kolos
27	60	22.2.	T/DAQ for the LHC experiments and upgrades	Francesca Pastore

Evening / Off topic || Before visit | Med Austron ||

Candidate lectures and lecturers

Title / subject of the lecture	Potential speaker	Additional information
	Hendrik Jansen	

The Trigger and DAQ system of the EUDET-type beam telescopes		
Lecture in trigger domain	Member of the HEPHY Vienna trigger group (e.g., Manfred Jeitler)	topic TBD (proposal: Level-1 trigger algorithms)
TBD	Mandakini Ravindra Patil	was a student in Rome
Waveform Digitizing and Signal Processing	Stefan Ritt, PSI	Has given a great lecture at ISOTDAQ in 2016 and blocked Vienna in his agenda.
Med Austron		

Status of the labs

Availability of tutors and equipment

Lab	CERN responsible	Description	Confirmed / Likely Tutors 2017	Orphaned sessions	Equipment to be provided by Vienna	Lab Book
1	Joos	VMEbus programming	Joos	0	1 monitor (with DVI-I cable), 1 keyboard (USB) and one mouse (USB)	
2	Joos	NIM	Pastore, Negri	0	One digital oscilloscope,	
3	Joos	NIM & scintillator	Ferrari, , Kordas	0	One digital oscilloscope, one voltmeter	
4	Joos	Muon DAQ	Pasqualucci, Ristic	0	1 monitor (with DVI-I cable), 1 keyboard (USB) and one mouse (USB)	
5	Sakulin	FPGA	Gigi / Zejdl	0	1 monitor (VGA), 1 keyboard (PS2) and one mouse (PS2)	
6	Sakulin	MicroTCA	Sakulin / Arnold or Wittmann (HEPHY)	0	1 monitor (VGA), 1 keyboard (USB) and one mouse (USB)	
7	Durante	LabView	Barreto	0	1 monitor (VGA or HDMI (not yet defined)), 1 keyboard (USB) and one mouse (USB), 1 Power Cable	
8	Durante	ADCs	Pospisil	0	One digital oscilloscope (possibly high-end), 1 monitor (VGA or HDMI (not yet defined)), 1 keyboard (USB) and one mouse (USB)	
9	von Haller	Networking	Le Goff	0	3 Desktop PCs running CC7 with root access, 2 with two 1G copper ports, 1 with three 1G copper ports, 1 monitor, 1 keyboard, 1 mouse. On each PC one of the network interface is for the university connection; if PCs not connected to the university network, 1 additional 4-port switch	ok
10	Sakulin	Micro controller	Feo, von Haller	0	1 monitor (VGA or HDMI (not yet defined)), 1 keyboard (USB) and one mouse (USB)	
11	Durante	Storage systems	Durante, Schwemmer	0	1 monitor (VGA), 1 keyboard (USB) and one mouse (USB) for the server	

					we'll ship, plus one desktop so more than one student can do the exercise in parallel	
12	von Haller	Control systems	Gamberini	0	2 monitors (with DVI cable), 2 keyboards (USB) and two mouse (USB)	ok
13	Durante	SoC FPGA	Patryk Oleniuk, Barros	0	2 monitors (VGA), 2 keyboards (USB), 2 mouses (USB)	ok
14	von Haller	Secret Lab: GPU	Lamanna	0	1 PC with an NVIDIA video card (not older than 5 years) running Linux with mouse, keyboard and monitor and a big screen.	

General: 20-30 IEC power cords

Candidate labs

Title / subject of the lap	Proposed by	Additional information
beam telescope	Hendrik Jansen (DESY)	proposal from 2015. Will not be in the program of Vienna
SoC FPGA	Manoel	Preparation ongoing, Will be in the program of Vienna as "lab 13"
GPU lab	Gianluca Lamanna	Will be in the program of Vienna as "secret lab"

Candidate tutors

Name	Background	Interested in exercise	financial status
Evgenia Voulgari	ISOTDAQ2015	2	UNCLEAR
Marcelo Vicente	ISOTDAQ2015	FPGA (ex13)	UNCLEAR
Ioannis Xiotidis	ISOTDAQ2016	2 (or 5)	UNCLEAR
Wes Gohn	Tutor in Budapest	1,2,3 and 6	TBD
Suerfu Burkhant<suerfu64@gmail.com>	ISOTDAQ 2017	5, 10 and others	unclear
Mandakini Ravindra Patil	ISOTDAQ 2011	TBD	Travel: institute. Hotel and Subsistence: ISOTDAQ

Availability of equipment

The lists items for the exercises that will have to be provided is on the ISOTDAQ sharepoint:
<https://espace.cern.ch/isotdaq-sharepoint/Shared%20Documents/listOfSupplies.xlsx?Web=>

This topic: Sandbox > DaqSchool2018

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