

# Table of Contents

<b>Welcome to the SystemOnChip Web Page.....</b>	<b>1</b>
SoC Interest Group.....	1
SoC Workshops.....	1
SoC Interest Group Meetings.....	1
SoC Organising Committee.....	1
SoC Overview Talks.....	1
on Zynq SoC or Zynq Ultrascale+.....	1
Tutorials on the use of Yocto and other tools.....	2
Projects using Yocto/OpenEmbedded frame work.....	2
Other talks about SoCs and work on them.....	2

# Welcome to the SystemOnChip Web Page

## SoC Interest Group

This is the twiki page of the Interest Group for "System-on-Chip":

- Email: if interested, please subscribe to the CERN mailing list: "system-on-chip@cern.ch"[↗](#). You can find the mail archive of the list [here](#)[↗](#).
- Mattermost - Chat Service: SystemOnChip Mattermost[↗](#)
- Discourse - Discussion Forum: SystemOnChip Discourse[↗](#)

## SoC Workshops

- 2nd SoC Workshop, 7-11 June 2021, CERN: <https://indico.cern.ch/event/996093/>[↗](#)
- 1st SoC Workshop, 12-14 June 2019, CERN: <https://indico.cern.ch/event/799275/>[↗](#)

## SoC Interest Group Meetings

- 23-NOV-2021 [↗](#) NEW!
- 16-FEB-2021 [↗](#)
- 06-OCT-2020 [↗](#)
- 29-APR-2020 [↗](#)
- 01-JUL-2020 [↗](#)

## SoC Organising Committee

The the SoC workshops and the meetings of the interest group are organised by the SoC Organising Committee:

- Marc Dobson (CERN), CMS TDAQ SysAdmin
- Revital Kopeliansky (Indiana University), ATLAS TDAQ Phase-2 deputy TC and DCS coordination
- Frans Meijers (CERN), CMS DAQ
- Diana Scannicchio (University of California, Irvine), ATLAS TDAQ SysAdmin
- Mamta Shukla (CERN), CERN-BE-CEM
- Ralf Spiwoks (CERN), CERN-EP-ESE and ATLAS L1CT (chair)

## SoC Overview Talks

- ACES 2020[↗](#): SoC: Overview and Outlook (R. Spiwoks)[↗](#)
- TWEPP 2019[↗](#): Using advanced SoCs at the CERN experiments and accelerator (M. Dobson)[↗](#)
- ACES 2018[↗](#): Overview of use of SoC (R. Spiwoks)[↗](#)

## on Zynq SoC or Zynq Ultrascale+

- Building/installing a CentOS root file system: CentOSForZynqMP. For CentOS8: CMS DAQ: CentOS8 rootfs[↗](#).
- Cross-compiling with a CentOS root file system: CrossCompileWithCentOS
- Setting up a crash kernel: CMS DAQ: CentOS8 crash kernel[↗](#)
- Using the IPMC Serial Payload Interface in U-Boot: u-boot-sipl git repository[↗](#)
- ATLAS MUCTPI: instructions to build and deploy CentOS

## Tutorials on the use of Yocto and other tools

- A tutorial on "How to get started with Yocto": [GettingStartedWithYocto](#)
- A tutorial on "How to prepare a boot bin file (FSBL)": [PreparingBootBinFile](#)
- A tutorial on "How to prepare a device tree blob (DTB)": [PreparingDeviceTreeBlob](#)

## Projects using Yocto/OpenEmbedded frame work

- ATLAS L1Calo: meta-l1calo: L1Calo's layer [↗](#)
- ATLAS L1Calo: ironman: gFEX's on-board python monitoring infrastructure [↗](#)
- Caribou Pixel Readout System: meta-caribou: Caribou's layer [↗](#)

## Other talks about SoCs and work on them

- ATLAS gFEX: I2C Communications (G. Stark) [↗](#)
- ATLAS gFEX: Understanding Layers (G. Stark) [↗](#)
- ATLAS gFEX: gFEX FDR Firmware review (G. Stark) [↗](#)
- Open-source ZYNQ-IPMC: xTCA Interest Group presentation (M. Vicente) [↗](#)
- Zynq-based Run Control for the ATLAS MUCTPI Upgrade: xTCA Interest Group presentation (R. Spiwoks) [↗](#)

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