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Welcome to the XTCA web
This is the TWiki home page of the xTCA Interest Group

Introduction

The xTCA Interest Group has been established during the TWEPP2010 workshop in Aachen. Its mission is to further the exchange of information between developers of xTCA components (both H/W and S/W) for application in HEP experiments or accelerators. The membership is open to all representatives of non-profit organisations. Anyone who would like to join is requested to contact the convener: Markus Joos

Meetings

It is planned to meet twice per year; either within the framework of a suitable HEP conference or virtually via EVO.

Past meetings

1st meeting

September 2010 @ TEWPP2010 in Aachen. Link to contributions

2nd meeting

March 2011 @ CERN: Link to contributions

3rd meeting

September 2011 @ TEWPP2011 in Vienna. Link to contributions

4th meeting

The 4th meeting took place at CERN on Tuesday, 17 April 2012. The agenda is available here.

5th meeting

The 5th meeting took place at on Wednesday, 19 September 2012 in Oxford as part of the TWEPP conference. The agenda with presentations is available here.

6th meeting

The 6th meeting took place at CERN on 9 April 2013. The presentations are available here.

7th meeting

The 7th meeting took place during the TWEPP 2013 conference on Thursday, 26 September at 17:00. The agenda with presentations is available here.

8th meeting

The 8th meeting took place at CERN on 17 March 2014 at 14:00. An slides of the presentation are available here.
9th meeting

The 9th meeting took place on Wednesday, 25 February 2015 at 14:00 in room 4-S-30. The agenda is here.

10th meeting

The 10th meeting took place on Thursday, 10 March 2016 at 15:00 in room 13-2-5. The agenda is here.

11th meeting

The 11th meeting took place on Thursday, 29 September 2016 in the late afternoon (~16:30 – 18:30) during the TWEPP 2016 conference.

12th meeting

The 12th meeting took place at CERN on Thursday, 15 June 2017 at 15:00 in room 40-R-D10. The agenda is here.

13th meeting

The 13th meeting took place at CERN on Friday, 27 April 2018 at 10:00 in room 13-2-5. The agenda is here.

14th meeting

The 14th meeting took place at CERN on Friday, 10 May 2019 at 15:00 in room 6-2-24. The agenda is here.

15th meeting

The 15th meeting will take place on Wednesday, 13 May 2020 at 15:00. At CERN room 13-2-5 has been reserved. Participation via Vidyo will be possible as well. The agenda is here.

Mailing lists

All participants will be added to the xtca-news@cernNOSPAMNOSPAMPLEASE.ch mailing list. This mailing list will be used to announce information relevant for the members of the xTCA Interest Group as well as xTCA related announcements of interest to CERN based people. In case you have problems posting mails to this list (because you don't have a CERN account) please contact one of the conveners.

Project repository

We are encouraging all participants to upload some brief information about their completed, current and future xTCA related projects to this TWiki table.

<table>
<thead>
<tr>
<th>Project name</th>
<th>Type</th>
<th>Short description</th>
<th>Status</th>
<th>Contact person</th>
<th>Institute</th>
<th>Link to further info</th>
</tr>
</thead>
<tbody>
<tr>
<td>uTCA @ DESY</td>
<td>various projects</td>
<td>An overview of uTCA projects at DESY for the European XFEL</td>
<td>Under development</td>
<td>Kay Rehlich</td>
<td>DESY</td>
<td>Link to DESY</td>
</tr>
<tr>
<td>AMC13</td>
<td>MicroTCA MCH</td>
<td>AMC (in MCH site) for clock/controls/DAQ</td>
<td>Deployed in CMS and other</td>
<td>Eric HAZEN</td>
<td>Boston University</td>
<td>AMC13 Page</td>
</tr>
<tr>
<td>Name</td>
<td>Type</td>
<td>Description</td>
<td>Functions</td>
<td>Developer</td>
<td>Institution</td>
<td>Website</td>
</tr>
<tr>
<td>-----------------</td>
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</tr>
<tr>
<td>APOLLO</td>
<td>ATCA</td>
<td>Platform for multiple ATCA blades</td>
<td>Prototyping</td>
<td>Eric Hazen</td>
<td>Boston University</td>
<td></td>
</tr>
<tr>
<td>GLIB</td>
<td>mid size, double width AMC</td>
<td>An evaluation platform and an easy entry point for users of high speed optical links</td>
<td>Under development</td>
<td>Paschalis VICHoudis</td>
<td>CERN</td>
<td></td>
</tr>
<tr>
<td>MMC_HW</td>
<td>28.5 x 20.5 mm mezzanine card for AMC board</td>
<td>A mezzanine implementing the Module Management Controller functions required by the µTCA standard</td>
<td>Available (as well as a ref. design)</td>
<td>Vincent BOBILLIER</td>
<td>CERN</td>
<td></td>
</tr>
<tr>
<td>MMC_SW</td>
<td>MMC SW for the Atmega128 the MMC mezzanine is based on</td>
<td>SW implementing the Module Management Controller functions required by the µTCA standard, including HPM.1 support</td>
<td>Available</td>
<td>Julian MENDEZ, Markus JOOS</td>
<td>CERN</td>
<td></td>
</tr>
<tr>
<td>GLIB_MMC_TEST</td>
<td>mid size, double width AMC</td>
<td>An evaluation platform for the MMC mezzanine carried by the GLIB</td>
<td>Under development</td>
<td>Paschalis VICHoudis</td>
<td>CERN</td>
<td></td>
</tr>
<tr>
<td>WR MCH</td>
<td>full size, single width MCH</td>
<td>Used in the White Rabbit switch</td>
<td>Under development</td>
<td>Tomasz WLOSTOWSKI</td>
<td>CERN</td>
<td></td>
</tr>
<tr>
<td>MINI-T5</td>
<td>full/mid size, double width AMC</td>
<td>Processing card for an upgraded Level-1 Calorimeter Trigger. Substantial connectivity: 160Gb/s optical, 64Gb/s LVDS</td>
<td>In system tests</td>
<td>Greg ILES</td>
<td>Imperial College London</td>
<td></td>
</tr>
<tr>
<td>ALB</td>
<td>mid size, double width AMC</td>
<td>An AMC load board based on the CPPM MMC that allows to control and monitor on-board dummy loads for crate power and cooling test purpose.</td>
<td>Available - In use for the evaluation tests of different µTCA crates</td>
<td>Vincent BOBILLIER</td>
<td>CERN</td>
<td></td>
</tr>
<tr>
<td>µTCA evaluation</td>
<td>Evaluations of commercial products</td>
<td>Test various properties of commercial µTCA components</td>
<td>Under development</td>
<td>Markus Joos</td>
<td>CERN</td>
<td></td>
</tr>
<tr>
<td>AMT</td>
<td>mid size, single width AMC</td>
<td>An easy to use MMC test board for the full functionality testing of the CPPM MMC.</td>
<td>Available - In use for testing MMC mezzanine cards</td>
<td>Vincent BOBILLIER</td>
<td>CERN</td>
<td></td>
</tr>
<tr>
<td>MicroHAL</td>
<td>Firmware+Software</td>
<td>Provides Hardware Access Library (HAL). Modular +</td>
<td>In system tests</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Project</td>
<td>Type</td>
<td>Description</td>
<td>Status</td>
<td>Author</td>
<td>Institution</td>
<td>WebLink</td>
</tr>
<tr>
<td>---------</td>
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<td>---------</td>
</tr>
<tr>
<td>IPBus</td>
<td>Firmware</td>
<td>Provides firmware for R/W/RMW + Block R/W register/memory access over UDP/IP. Wishbone compatible.</td>
<td>In system tests</td>
<td>Dave Newbold</td>
<td>University of Bristol</td>
<td>WebPage</td>
</tr>
<tr>
<td>ControlHub</td>
<td>Software</td>
<td>Provides a uniform interface between the hardware access methods (e.g. IPBus, VME, USB) and the higher-layer abstractions (e.g. MicroHAL).</td>
<td>In system tests</td>
<td>Rob Frazier</td>
<td>University of Bristol</td>
<td>WebPage</td>
</tr>
<tr>
<td>Redwood</td>
<td>Software</td>
<td>software layer which maps registers onto C++ objects. Can talk to the ControlHub or directly to IPBus devices.</td>
<td>In system tests</td>
<td>Andrew Rose</td>
<td>Imperial College London</td>
<td>WebPage</td>
</tr>
<tr>
<td>ATCA @ IPFN for Nuclear Fusion</td>
<td>Several Projects</td>
<td>Overview of several projects for nuclear fusion applications including ATCA IO boards and RTMs and a controller</td>
<td>Several completed and several ongoing developments</td>
<td>Bruno Soares Gonçalves</td>
<td>IPFN-IST</td>
<td>WebPage</td>
</tr>
<tr>
<td>ABBA</td>
<td>Various projects</td>
<td>A set of boards for mastering ATCA and preparing upgrade of ATLAS LAr Back End electronics: ATCA Controller Mezzanine (hardware &amp; software) + ATCA Test Board + ATLAS LAr ROD Evaluator</td>
<td>Under development</td>
<td>Guy PERROT</td>
<td>LAPP</td>
<td>LAPP presentation</td>
</tr>
<tr>
<td>Sirius BPM back-end</td>
<td>double-width AMC</td>
<td>Double-width AMC with dual high-pin count FMC slots for digital signal processing, data acquisition and feedback control in beam position monitoring (BPM) systems.</td>
<td>Under development</td>
<td>Daniel Tavares</td>
<td>CNPEM/LNLS</td>
<td>link</td>
</tr>
<tr>
<td>MP7</td>
<td>double-width AMC</td>
<td>General purpose data processing with Xilinx Virtex-7 FPGA &amp; 740 Gbps of</td>
<td>Available</td>
<td>Greg Iles</td>
<td>Imperial College London</td>
<td>link</td>
</tr>
</tbody>
</table>
### Other information

- Access to PICMG standards (for people at CERN)
- MTCA.4 workshop at DESY (11 & 12 December 2012). The presentations are available here
- MTCA.4 for Industry and Research. A DESY / Helmhotz activity
- Download: Pinout for JTAG Switch Module

### Company presentations

**Elma, 14.2.2013**

- CERN_ATCA_Presentation_130214.ppt
- CERN_MicroTCA_System_architecture_1302014.ppt
- CERN_MTCA_presentation.ppt
XTCA Web Utilities

- advanced search
- WebTopicList - all topics in alphabetical order
- WebChanges - recent topic changes in this web
- WebNotify - subscribe to an e-mail alert sent when topics change
- WebRss, WebAtom - RSS and ATOM news feeds of topic changes
- WebStatistics - listing popular topics and top contributors
- WebPreferences - preferences of this web

This topic: XTCA > WebHome
Topic revision: r84 - 2020-05-11 - ParaschosMoschovakos