

Resource	Chimaera design (4 SCT modules) Spartan-6 XC6SLX25	UniGe USB3 GPIO board Cyclone V A7	Ratio (UniGe/Cambridge)
Logic cells/ Logic elements	24k	150k	
Equivalent registers	30k	226k	7.5
RAM	936 kB	6860 kB + 836 kB	8.2
18 × 18 DSP blocks	38	156 × 2	8.2
PLLs	2	7	3.5
Chimaera design usage (4 modules)	45% Logic 50% RAM	6.0% Logic 6.1% RAM	
UniGe USB3 library		1% Logic 0.5% RAM	
Total for 24 modules + USB3 library		36.0% Logic 37.1% RAM	