

UA43 Dry-Run RAT Réunion Avancement Travaux

HARDWARE COMMISSIONING COORDINATION

27 September 2006 9:00 – CCC

Present: Alejandro Castañeda, François Chevrier, Guy Crockford, Knud Dahlerup-Petersen Robert Harrison, Robin Lauckner, Herve Milcent, Adriaan Rijllart, Félix Rodríguez-Mateos, Antonio Vergara

Progress of the UA43 Dry-Run

- Summary of the present situation by Robin:
 - As explained yesterday (see RAT minutes of 09/26), PIC1 and PIC2 procedures seem to last much longer than expected due to the time needed to send the PM data buffers and get the OK from QPS, close the switches and reset the system.
 - It was proposed to speed the sequence up by carrying out the test in circuit batteries whenever possible.
 - A problem of battery tests is that they can screen possible cross-talk between PIC signals (i.e. bad wiring of PIC).
- It is decided that for the tests of today:
 - The PIC1 procedure will be tested without sending the PM data (i.e. after the test the sending of PM data is skipped and immediately after the QPS expert resets the system in order to move to the following circuit or test step). We will evaluate how much time we save with respect to the strategy applied so far (i.e. PM data sent and wait for QPS OK).
 - The tests will be carried out today from 14:00 to 17:00 with the presence of experts from:
 - QPS
 - Post-Mortem
 - PIC
 - HCC

Open Issues

- Controls
 - Some discussions are going on about the alarms. The avalanche of alarms issue must be studied in detail. Robin will follow this up. No news.

- QPS
 - Some works at the hardware level still have to be carried out for the 13kA QLC (PCB production). It will be ready by the end of the run. No news

Test Schedule

Week 39 (09.25 – 09.29)

- Parallelism and timescales for commissioning
- Battery tests of B1 and B2 circuits.
- PM data analysis (global, connection to sequencer)

Week 40 (10.02 – 10.06)

- Validation of PIC1 and PIC2 test procedures as implemented in the sequencer for interface type A with dummy quench loop
- Parallelism and timescales for commissioning including type A
- PM data analysis (global, connection to sequencer)
- PLI and PNO procedures
- Global protection mechanisms (including AUG and UPS fault simulation)
- Others



Next Dry-Run RAT in CCC: Thursday 28th at 9:00

Antonio Vergara