

UA43 Dry-Run RAT Réunion Avancement Travaux

HARDWARE COMMISSIONING COORDINATION

6 October 2006 9:00 – CCC

Present: Juan Blanco, François Chevrier, Robert Harrison, Adriaan Rijllart, Iván Romera, Antonio Vergara

Progress of the UA43 Dry-Run

- Today (10.05) François will complete the tests before 15:30 and will give green light to David Nisbet to *consigné* the power converters in UA43.
- François will also run PIC1 and PIC2 procedures in order to send some post-mortem data to Robert for checking the PIC PM analysis tools.
- There is a problem in the communication between RQ7 and the PIC that is being repaired.
- Antonio informs that SACEC has proposed to do the next dry-run in UA83 after the DC cables have been connected to the current leads (electrically isolated from them). Since the temporal short-circuit installed by MEL will not stand high currents, the tests during the dry-run will be carried out at a maximum current of i_{min_op} . A hardware limitation will be placed in the power converters and the sequencer will be modified not to overpass such current.
- HCC will negotiate with planning how this strategy can enlarge the time allocated for the dry-run. François would prefer to have shorter dry-runs with sometime between them that just one 4-week dry-run.



Antonio Vergara