

HARDWARE COMMISSIONING COORDINATION

October 13, 2006, 8.30 – RR53 and USC55

Present: Loïc Bourgeois, Knud Dahlerup-Petersen, Claude Dehavay, Julien Kis, Sebastien Maquet, Jean-Pierre Malod'Ognin, David Nisbet, Rosario Principe, Jaques Toullieux.

SCT in UA63

Present situation

- EL: earth faults during the "mise en service" on Tuesday, Oct 10
Jack confirms that the I_0 need to be verified. An intervention of some 30 minutes is scheduled for next Friday, Oct 21, after the test ABPO.
- CV: demineralised water off yesterday Oct 12. Off again this morning ("debit de fuite"). The fault could be originated by the filling of a ROESE station. The analysis of the failure is ongoing (Loic).
- "Balisage" of the UA63 in place.
Installation of the flashing boxes for the short-circuit parts finished today.
"Baches" for the protection of the short-circuit parts: installation completed.
- ABPO: 10 out of 12 PCs 120A tested. Test of the 600a already started.
- EE: 10 out of 16 EE systems 600A already tested. These tests will be completed by next Tuesday, Oct 17.
The 13kA EE tests are ongoing. They could be finished by Wednesday, Oct 18.
Interlock tests could be anticipated and executed during week 43.
- EE resistances QRQ: drying procedure completed. Equipment successfully tested at 1000V.
- Wifi operational. A second box will be installed today, Oct 13, in the UJ in order to improve the efficiency of the system in the area.
Remember: racks CYNET closed with a key. Not accessible for cabling. In case of intervention, IT personnel must be contacted in order to make access to the racks possible (contact person: Olaf Van Der Vossen 163038).
- Demineralised water circuit: the balancing of the circuit is completed. No problems encountered on the filters during the check by the CV team.

The Elettas 9, 10, RQF, RQD and RB, will be jointly checked by EL and CV this morning.

- For comments: TSIC proposes a date for the “endurance tests” in point 5. December 4.
- New schedule for the future SCT areas on the Hardware Commissioning web page:

<http://hcc.web.cern.ch/hcc/schedules.html>

Heat Runs and Test Schedule

27.10	8-h heat run: 120 and 600A.
03.11	8-h heat run: RB.
06.11	8-h heat run: RQ and 2kA.
08.11	24-h heat run.

Open Issues

09.10	Installation of the FCR (reduced size) by TSIC.
09.10	Ventilation caps on the cable ducts between UA and RA.
09.10	Elettas calibration by CV.

Closed Issues

05.10	Fire detection installed.	05.10
05.10	Short circuit parts in UA63.	10.10
09.10	Cleaning of the area.	10.10
09.10	“Tableau BT” tested. Conformity certificates ok.	10.10
09.10	Detailed schedule SCT in UA63 available.	10.10
09.10	Wifi operational.	10.10
09.10	“Balisage” of the UA63 in place.	11.10
09.10	Worldfip operational. Gateway operational.	11.10
10.10	Installation of the FGC completed.	10.10
09.10	EE, “test isolement” on the QF and QD. Done.	13.10
09.10	Water leakage on the RQF to be verified. Done.	13.10
09.10	ED water circuits balancing. Done.	13.10
09.10	Few modules 120 and 600A to be installed. Done.	13.10
09.10	Flashing boxes and “baches” for the short circuit parts.	13.10



Next RAT meeting
Tuesday October 17, 8.30 in point 6 (SX6)
Room 2685/1-002

RP