

RAT in Point 8 Réunion Avancement Travaux

HARDWARE COMMISSIONING COORDINATION - WEEK 19

10 May 2007 08:30 in 2889-R-009

Present:

AB/CO:	R. Schmidt, M. Zerlauth
AB/PO:	D. Nisbet, Y. Thurel, H. Thiesen, S. Page
AB/OP:	L. Normann
AT/MEL:	S. Feher, G. Kirby, B. Flora, KH. Mess, A. Ballarino, S. Le Naur, R. Denz, K. Dahlerup-Petersen, M. Lamm
AT/MTM:	A. Siemko
AT/ACR:	F. Millet, R. Rabehl, L. Ronayette, L. Serio
TS/HDO:	R. Saban, M. Pojer, MP. Casas Lino, M. Solfaroli, A. Vergara, B. Perea Solano, B. Bellesia

Sector 78

- F. Millet confirms conditions OK for powering the circuits of the DFBMA and DFBMC at low current (maximum of 10A for the correctors and 100A for Q5, Q4 and D2).
- PCC and PIC2 tests can be started for the following circuits:
 - RCBYVS4.L8B1
 - RCBYVS4.L8B2
 - RCBYHS4.L8B1
 - RCBYHS4.L8B2
 - RCBYV4.L8B1
 - RCBYH4.L8B2
 - RCBCH5.L8B1
 - RCBCV5.L8B2
 - RQ4.L8
 - RQ5.L8
 - RD2.L8
- PIC2 should not start till PCC has been completed for all the circuits. See detailed planning for today in:
<https://twiki.cern.ch/twiki/bin/view/HCC/BlogEntryPoint8x2007x05x10x08x17?point=8>
- The CRYO_MANTAIN and CRYO_START signals are not enough for monitoring the tests of today, AT/ACR should make the cryogenics information visible in real time (TIMBER delay is not acceptable) from the FCR.
- Before starting the tests, Markus will check the proper propagation of the CRYO_MANTAIN and CRYO_START signals from cryo to the converters via the powering interlock controller.

- The issue regarding loss of logging data from the converters once an existing SOC is modified has been now solved. Antonio recommends to keep an eye on the Logging supervision tool during today's test to be sure we are not losing data.
- The RQT12.L8B1 and RQT12.L8B2 converters, which are currently short-circuited at the level of the DFBA, will be made available today, after intervention of Reiner, for testing of the RQD2 powering procedure. The Sequencer developers will be informed once the converters are ready.
- Reiner informs that during the following days (not today) a boiling-off of one DFBM while powering at very low current some 120A leads should be carried out in order to monitor the voltage drop across the lead.
- Currently the loss of the cryogenic conditions makes the PIC remove the powering permit to the converters which makes the converter initiate a slow power abort. This, for the Q4 main circuit and its correctors, may take up to 20 minutes. Rudiger proposes to investigate whether the protocol should be changed in order to bring the current faster to zero.
- Next meeting Friday, 11 May at 8:30 in 2889-R-009

Antonio Vergara

Open Hardware Commissioning Issues in SECTOR 78

REGION	ISSUE
SECTOR 78	
	QPS voltage tap problem in quadrupole 33R7 - Another tap will be used instead. Attention because the damaged tap might be floating on the conductor.
	Non-conformity of the crates of cryo instrumentation (inrush current) (A.Suraci)
	Securing of the ventilation units
	Four circuits in Q5 suffer a breakdown at around 450V due to a weak insulation. The four circuits are RCBCVS5.L8B1, RCBCHS5.L8B2, RCBCHS5.L8B1 and RCBCVS5.L8B2. Insulation towards ground and other circuits is OK.
	EI_QA performed on C16L8. ICC test showed reversed sequence of V-taps on circuit RCBV16.L8B1 (D.Bozzini) check
	BPM connection in Q2 (R.Jones) ? waiting for Inner Triplet to be repaired
	MB1055 magnet to be changed before powering above 2kA RB.A78
	Inner Triplet in Point 8 to be repaired
	Failure of supports (red jacks) of D2-Q4 in L8 - temporary repair in place. EDMS document "Major movements of the D2-Q4 magnets and supports in 8L" https://edms.cern.ch/document/833365/1
	Water leak on the tunnel concrete wall to be fixed (C33L8).

Closed hardware commissioning issues in sector 78 can be found at <http://hcc.web.cern.ch/hcc/activities/activities.php?region=S78>.