

Circuits	Type	DFB	Temp.	Status
RB.A78	MB	DFBAO.7L8	1.9	DONE
RQF.A78	MQ	DFBAO.7L8	1.9	DONE
RQD.A78	MQ	DFBAO.7L8	1.9	DONE
RQT12.L8B1	MQT	DFBAO.7L8	1.9	DONE
RQTD.A78B1	MQT	DFBAO.7L8	1.9	Blocked by ACR
RQTD.A78B2	MQT	DFBAO.7L8	1.9	Blocked by ACR
RQTF.A78B1	MQT	DFBAO.7L8	1.9	OK for PCS
RQTF.A78B2	MQT	DFBAO.7L8	1.9	OK for PCS
RSD1.A78B1	MS	DFBAO.7L8	1.9	Blocked by ACR
RSD1.A78B2	MS	DFBAO.7L8	1.9	OK for PCS
RQS.L8B1	MQS	DFBAO.7L8	1.9	OK for PCS
RSD2.A78B1	MS	DFBAO.7L8	1.9	OK for PCS
RSD2.A78B2	MS	DFBAO.7L8	1.9	Blocked by ACR
RSF1.A78B1	MS	DFBAO.7L8	1.9	OK for PCS
RSF1.A78B2	MS	DFBAO.7L8	1.9	OK for PCS
RSF2.A78B1	MS	DFBAO.7L8	1.9	Blocked by ACR
RSF2.A78B2	MS	DFBAO.7L8	1.9	Blocked by ACR
RCO.A78B1	MCO	DFBAO.7L8	1.9	DONE
RCS.A78B1	MCS	DFBAO.7L8	1.9	Blocked by ACR
RCS.A78B2	MCS	DFBAO.7L8	1.9	Blocked by ACR
RQS.A78B2	MQS	DFBAN.7R7	1.9	Blocked by EIQA
RQS.R7B1	MQS	DFBAN.7R7	1.9	Blocked by EIQA
RQTL10.R7B1	MQTLI	DFBAN.7R7	1.9	Blocked by EIQA
RQ6.R7B1	MQTLH	DFBMH.6R7	4.5	OK for PCS
RQ6.R7B2	MQTLH	DFBMH.6R7	4.5	OK for PCS
RQ4.L8		DFBMA.4L8	4.5	DONE
RQ5.L8		DFBMA.4L8	4.5	DONE
RD2.L8		DFBMC.4L8	4.5	DONE
RCBCH5.L8B1		DFBMC.5L8	4.5	DONE
RCBCV5.L8B2		DFBMC.5L8	4.5	DONE
RCBYH4.L8B2		DFBMA.4L8	4.5	DONE
RCBYHS4.L8B1		DFBMA.4L8	4.5	DONE
RCBYV4.L8B1		DFBMA.4L8	4.5	DONE
RCBYVS4.L8B1		DFBMA.4L8	4.5	DONE
RCBYVS4.L8B2		DFBMA.4L8	4.5	DONE
RCBH11.L8B1			1.9	DONE
RCBH12.L8B2			1.9	DONE
RCBV11.L8B2			1.9	DONE
RCBV12.L8B1			1.9	DONE

Antonio Vergara
Hardware Commissioning Coordination
27-Jun-07

Completed	21 / 39
at 4.5K	12 / 12
at 1.9K	9 / 27