

Frame1

	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0	Field	
0	Idle sequence																Frame sync	
1	Idle sequence																	
2	H1	H2	H3	H4													L0Event number	Global Identification
3	-			Board #				L0_B_Id										
4	BCSU PT1						BCSU PT2											
5	BCSU Address candidate 1																BCSU Outputs	
6	BCSU Address candidate 2																	
7	F1	F2	F3	F4	PU#1 st	PU#2 nd	BCSU Status					BCSU BCId						
8	PU0 PT1						PU0 PT2											
9	PU0 Address candidate 1																BCSU Inputs	
10	PU0 Address candidate 2																	
11	-																	
12	PU1 PT1						PU0 Status					PU0 BCId						
13	PU1 Address candidate 1																	
14	PU1 Address candidate 2																	
15	-						PU1 Status					PU1 BCId						
16	PU2 PT1						PU2 PT2											
17	PU2 Address candidate 1																	
18	PU2 Address candidate 2																	
19	-																	
20	PU3 PT1						PU2 Status					PU2 BCId						
21	PU3 Address candidate 1																	
22	PU3 Address candidate 2																	
23	-						PU3 Status					PU3 BCId						
24	PU0 Inputs bits 463 to 0 (see format next table)																PU0 Inputs	
25																		
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35																		
36																		
37																		
53	PU1 inputs bits 463 to 160 (see format next table)																PU1 Inputs	
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Frame2

	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0	Field
0	idle sequence																Frame sync
1	idle sequence																
2	-	L0Event number															Global Identification
3	-	L0_B_Id															
4	PU2input bits 159 to 0 (see format next table) -																PU1 inputs
5																	
6																	
7																	
8																	
9																	
10																	
11																	
12	PU2Inputs bits 463 to 0 (see format next table) -																PU2 Inputs
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26																	
27																	
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29																	
30	PU3 Inputs bits 463 to 0 (see format next table)																PU3 Inputs
31																	
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38																	
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PUfield

	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0	Field																																																																				
0	-							Optical links error flags							Signalization																																																																						
1	-	Serial neighbours error flags					Parallel neighbours error flags																																																																														
2	Optical links inputs																																																																																				
3																		M1 optical Inputs																																																																			
4																																			M2 optical Inputs																																																		
5																																																				M3 optical Inputs																																	
6																																																																					M4 optical inputs																
7	M5 optical inputs																																																																																				
8	Neighbours transferred at 1.6 Gb/s																																																																																				
9																		1st backplane serial bus																																																																			
10																																			2nd backplane serial bus																																																		
11																																																				3rd backplane serial bus																																	
12																																																																					4th backplane serial bus																
13																																																																																					
14	6 th backplane serial bus																																																																																				
15	5 th bp // 4 th bp // 3 rd bp // 2 nd bp //																																																																																				
16	1 st bp //																																																																																				
17	Crossing vertical bus																																																																																				
18	Neighbours transferred at 80 MHz																																																																																				
19																		Horizontal parallel bus																																																																			
20																																			Vertical parallel bus																																																		
21																																																																																					
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