

IBERT for Pulsar**11b**

Zijun

Jun 4

FGPA info

- xc7vx690tffg1927-2

IBERT setup 0

Re-customize IP

IBERT 7 Series GTH (3.0)

Documentation IP Location Switch to Defaults

Component Name

Protocol Definition Protocol Selection Clock Settings Summary

Silicon Version

General ES/Production
 Initial ES

The maximum number of quads available for this device is 20

Number of Protocols

Protocol	LineRate(Gbps)	DataWidth	Refclk(MHz)	Quad Count	Quad PLL
Custom 1	10.0	32	200.000	11	<input type="checkbox"/>

QPLL
CPLL

Change as you want

OK Cancel

Ports:

- RXN_I[43:0]
- RXP_I[43:0] TXN_O[43:0]
- GTREFCLK0_I[10:0] TXP_O[43:0]
- GTREFCLK1_I[10:0] RXOUTCLK_O
- SYSCLK_I

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Re-customize IP

IBERT 7 Series GTH (3.0)

Documentation IP Location Switch to Defaults

Component Name: `ibert_7series_gth_0`

Protocol Definition Protocol Selection Clock Settings Summary

Please select Protocol-Quad combination

GTH Location	Protocol Selected	Refclk Selection	TXUSRCLK Source
QUAD_210	Custom 1 / 10.0 Gbps	MGTREFCLK0 211	Channel 0
QUAD_211	Custom 1 / 10.0 Gbps	MGTREFCLK0 211	Channel 0
QUAD_212	Custom 1 / 10.0 Gbps	MGTREFCLK0 211	Channel 0
QUAD_213	Custom 1 / 10.0 Gbps	MGTREFCLK0 214	Channel 0
QUAD_214	Custom 1 / 10.0 Gbps	MGTREFCLK0 214	Channel 0
QUAD_215	Custom 1 / 10.0 Gbps	MGTREFCLK0 214	Channel 0
QUAD_216	Custom 1 / 10.0 Gbps	MGTREFCLK0 217	Channel 0
QUAD_217	Custom 1 / 10.0 Gbps	MGTREFCLK0 217	Channel 0
QUAD_218	Custom 1 / 10.0 Gbps	MGTREFCLK0 219	Channel 0
QUAD_219	Custom 1 / 10.0 Gbps	MGTREFCLK0 219	Channel 0
QUAD_110	Custom 1 / 10.0 Gbps	MGTREFCLK0 110	Channel 0
QUAD_111	Custom 1 / 10.0 Gbps	MGTREFCLK0 110	Channel 0
QUAD_112	Custom 1 / 10.0 Gbps	MGTREFCLK0 112	Channel 0
QUAD_113	Custom 1 / 10.0 Gbps	MGTREFCLK0 112	Channel 0
QUAD_114	Custom 1 / 10.0 Gbps	MGTREFCLK0 115	Channel 0
QUAD_115	Custom 1 / 10.0 Gbps	MGTREFCLK0 115	Channel 0
QUAD_116	Custom 1 / 10.0 Gbps	MGTREFCLK0 115	Channel 0
QUAD_117	Custom 1 / 10.0 Gbps	MGTREFCLK0 118	Channel 0
QUAD_118	Custom 1 / 10.0 Gbps	MGTREFCLK0 118	Channel 0
QUAD_119	Custom 1 / 10.0 Gbps	MGTREFCLK0 118	Channel 0

Show disabled ports

- RXN_I[79:0]
- RXP_I[79:0] TXN_O[79:0]
- GTREFCLK0_I[19:0] TXP_O[79:0]
- GTREFCLK1_I[19:0] RXOUTCLK_O
- SYSCLK_I

Active the Quads you need

OK Cancel

IBERT setup 2

Re-customize IP

IBERT 7 Series GTH (3.0)

Documentation IP Location Switch to Defaults

Component Name:

Protocol Definition Protocol Selection **Clock Settings** Summary

RXOUTCLK Probe

Add RXOUTCLK Probes

Clock Type	Source	I/O Standard	P Package Pin	N Package Pin	Frequency(MHz)
System Clock	External	LVDS	AU15	AV15	200.00

System Clock Termination Settings

Enable DIFF Term

Ports:

- RXN_I[43:0]
- RXP_I[43:0]
- GTXN_O[43:0]
- GTXP_O[43:0]
- GTREFCLK0_I[10:0]
- GTREFCLK0_O[10:0]
- GTREFCLK1_I[10:0]
- GTREFCLK1_O[10:0]
- SYSCLK_I

OK Cancel

Example Codes

- C:\Users\zixu-local\Documents\Vivado_firmware_develop\ibert_for_pulsarIb