L25: ‘a CBC3 chip’ - ‘the CBC3 ASIC’

Figure 4 - since for the output of the stubs you quote the data rate as 5x320 Mbps perhaps for consistency its better to also change the centroids in (from the SSA) to 8x320 Mbps?

L95: ‘and can be tuned against an external reference to ensure the same charge is delivered to each channel’ - I know what you are saying, but I still read this and think that you mean that the adjustment on the DC level allows you to adjust for differences between channels whereas I think the intention is to adjust across chips?

L197: ‘for a detection threshold of 4800 e−’ - just noticing this now - you mention a ‘default’ settings of 6200 e− at the beginning of section 5 (5.1) - but here 4800 e− was chosen as an operating point but no reason is given for that choice.

Section 5.5 I might have missed it but I don’t see a reference for which threshold these angular scans were taken at.