Pin Assignment: RD53A SCC $\Rightarrow$ CERN FMC Card $\Rightarrow$ FC7

- Work is in progress...
- Based on RD53A SCC Card, CERN FMC Card and FC7 schematics
- Connections between CERN FMC Card and FC7 not checked yet, the signal directions in all cases also need to be revised.
Pin Assignment: RD53A SCC $\Rightarrow$ CERN FMC Card $\Rightarrow$ FC7

- work is in progress...
- should be investigated in both directions: RD53A SCC $\leftrightarrow$ FC7
- with the help of the pin assignment firmware porting will be straightforward
FC7 golden firmware: IPBus simulation, register loopback

- IPBus test bench is useful for debugging purposes for example: read/write procedures that would be executed by a CPU
- ctrl-status register read/write procedure is tested with python scripts
aurora 64b/66b: Example design with simulation

- Tested and simulated three-lane configuration using a single platform in loopback mode based on XAPP1192
- Next step is to port aurora IP to the golden firmware (check XAPP1227 for asymmetric-lane-design)
- IPBus registers + aurora + RD53A simulator