IT-firmware development update

Current state of AMC13 & IT-firmware

→ IT-firmware development branch contains AMC13 and TTC decoder blocks
→ Of course, the firmware contains the necessary constraints and signals as well
→ BUT series of AMC13 & FC7 in \( \mu \)TCA crate testing revealed that there is a timing issue
→ the timing problem do not allow the TTC decoder block to accept L1A trigger from AMC13
→ the source of the timing problem is inside the command processor block

Workaround

1. bug should be removed from the command processor block
2. fast command block should be initialized in a way it accepts L1A triggers from the TTC decoder block
3. After solving 1. and 2. the TTC decoder block would be able to accepts L1A triggers from AMC13

misc

→ FC7 clocking diagram for IT-firmware will be uploaded to Escape site soon

https://espace.cern.ch/Tracker-Upgrade/DAQ/SitePages/Home.aspx